REGISTRATION DETAILS
Participants are requested to register by filling in the accompanying slip and sending the same to the coordinators. The registration fee (non-refundable) is as follows:

- Industry Persons Rs. 700/-
- Faculty and Research Scholars Rs. 500/
- PG Students 400/-
- Mode of payment: DD drawn in favour of “The Principal, PSNA College of Engg. & Tech.” payable at Dindigul

Maximum Number of participants : 35
(First come first serve basis)
Boarding & Lodging will be provided with reasonable charge in college Hostel for the participants who are coming from remote places.

IMPORTANT DATES
Last date for receipt of application : 31-03-2016
Intimation of selection : 06-04-2016

CONTACT DETAILS:
(Forms and DD can be sent the below mentioned address)
Dr. P. Thirumurugan, Assistant Professor,
Department of ECE,
PSNA college of Engineering and Technology,
Kothandaraman Nagar
Dindigul 624622, Tamilnadu, India
E-mail : thiruji.murugan@gmail.com
Mobile : 9843430603

Note
1) Please attach a photocopy of College ID card.
2) Use photocopy of the registration form, if required
ABOUT THE INSTITUTIONS

PSNA CET is a premier institution in Tamil Nadu with International standards. It was established in 1984 at the outskirts of Dindigul Corporation as a response to the clarion call made by the public for a quality engineering institution in this part of South Tamil Nadu. This prestigious institution, at present, offers 7 UG courses and 8 PG courses in Engineering besides MCA and MBA. The students in India, by and large, are inundated with plenty of choices at the higher education level due to steady rise in educational institutions. Any student who has just completed his higher secondary course and who does not have a specific plan will be bewildered forever in choosing a good institution from among many. As an answer to this questions whether there are any parameters which enable an aspirant to demarcate a good institution from the rest, there are two essential parameters. They are the award of accreditation given by NBA of AICTE, New Delhi and the Best Engineering College Award given by ISTE, New Delhi. PSNA CET was fittingly given the Bharatiya Vidya Bhavan National Award for an Engineering College having Best overall performance for the year 2007 at the National level and all UG programmes were awarded accreditation status by NBA in 2013.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1984 with an intake of 60 undergraduate students. Since then the department has come a long way and is now regarded as one of the premiere departments of the institution with an intake of 300 undergraduate students and 61 postgraduate students per year. The Department offers two PG courses, M. E in Applied Electronics and M. E in VLSI Design. The Department has been accredited by the National Board of Accreditation (NBA) for a period of 5 years effect from 2013. The department is recognized as research centre by Anna University, Chennai. To motivate the research activity among faculty members, they are grouped under six research groups like Image processing group, VLSI group, Signal processing and Communication group, Information Retrieval group, Networking group and Medical Electronics group to carry out the research in the advanced research fields. The excellent placement record has been consistently above 85 % for the past years. Our students got placed in companies like Tata Consultancy Services, Cognizant Technology Solutions, Wipro Technologies, Infosys Technologies, Jasmine InfoTech, Soliton Technologies, Benchmark Broadcast Systems Pvt. Ltd. and HCL Technologies Limited.

OBJECTIVE OF THE WORKSHOP

The embedded system applications offer direct face-to-face medical support to remote supervision and monitoring automated devices for control of long-term medication and treatment. This workshop will give an idea to design a standalone system.

VLSI systems and platforms are in widespread use in medical practices to improve healthcare diagnosis, monitoring, and therapy. They are used for Wireless Bio Sensing, Lab-on chip and Ultra - Low Power CMOS image sensors and find its application in wireless capsule endoscopy and as an artificial retina. This workshop will give an idea about on-chip processing and processors/DSPs for image and signal processing application.

Advances in technology and automation have minimized the requirement of mental functions such as information processing and decision analyses even in carrying out important tasks. There is a need to maintain an optimal level of cognitive load on the brain, especially in critical decision making fields, such as air traffic control, fire safety and military operations, where the imposed task load can be intermittently high and may lead to decreased performance or even failed task completion. This workshop provide different signal processing techniques and interpretation of brain signal towards neuroscience approach.

ORGANISING COMMITTEE

Mr. R. Sathesh Raaj, AP / ECE
Mr. A. Selva Pandian, AP / ECE
Mr. R. Bala Kumaresan, AP / ECE
Mr. S. Bharath, AP / ECE
Mr. P. Rajasekaran, AP / ECE

TOPIC S TO BE COVERED

- Neuroscience in health care
- VLSI in medical application
- Hand on training for medical device Trouble Shooting
- Hand On training
  - Matlab with Medical Signal Processing Tool Box
  - TCAD
  - Arduino Software (IDE) embedded system

TARGET AUDIENCE

Research Scholars, Academic Faculty Members, PG Students and Industrial persons interested in human health care